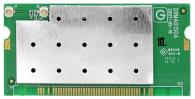


802.11 b/g 54Mbps wifi mini-PCI module, half-size, MB55/AR2417

Model: DRMA-81



DRMA-81 is an IEEE802.11 b/g 54Mbps wifi mini-PCI module in half-size type IIIA form factor with two U.FL connectors designed specifically for integration in performance-critical applications with space limitation. Supporting 802.11g 54Mbps, DRMA-81 has the capability of full size mini-PCI in half-size IIIA form factor at competitive price. It is ideal for embedding into new or existing ergonomic devices such as gateway/routers, notebooks, and application-specific devices (ASDs) used in vertical market.

Linux 2.6 AP/Router driver reference code support enables gateway/router manufacturers to provide products that enjoy improved wifi performance and time to market through trouble-free WiFi integration.

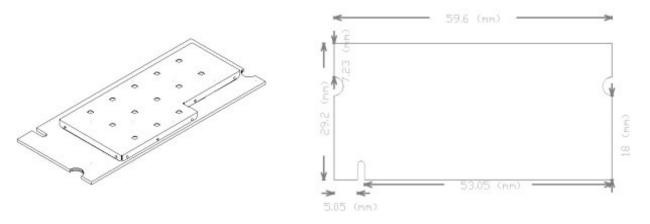
Key Features:

- Half-size mini-PCI Type IIIA form factor is ideal for embedding into new or existing performance-critical applications with space limitation.
- Windows 98SE/ME/2000/XP/Vista driver and comprehensive client utility supports provide immediate 11b/g wifi and management capability.
- Linux 2.6 AP/Router driver reference code enables trouble-free AP/Gateway design.
- Supported by MADWiFi providing Linux kernel drivers for industrial, academic, or personal projects at highest flexibility and lowest cost.
- Supports 802.11g/11b auto fallback data rate and seamless roaming between 802.11b, and 802.11g multiple AP wifi networks.
- Future support of 802.11d (Regulatory Domain), 802.11e (Quality of Service, WMM), and 802.11h (TPC/DFS/ DFS2) by software upgrade.
- Country code selector provides flexibility to change regulatory domains.
- Hardware radio on/off mechanism provides highest design flexibility for integrators.
- Hardware encryption of WEP/WPA/WPA2 security is ideal for performance-critical devices.
- Supports WEP/WPA/WPA2, IEEE802.1x (EAP-TLS, EAP-PEAP, LEAP), and LEAP/CCX3.0 providing advanced level
 of LAN security.
- Dual Hirose U.FL antenna connectors enable transmit and receive diversity for flexible RF design.
- RoHS 2002/95/EC compliance meets environment-friendly requirement.

EEPROM DISCRETE DISCRETE ANT BALUN LPF TX CONNECTOR 31 Mini PCI interfaces DPDT AR2417 SWITCH PCI ANT DISCRETE DISCRETE Mini DC3.3V 32 RX BALUN LPF 40MHZ XTAL

Hardware Block Diagram

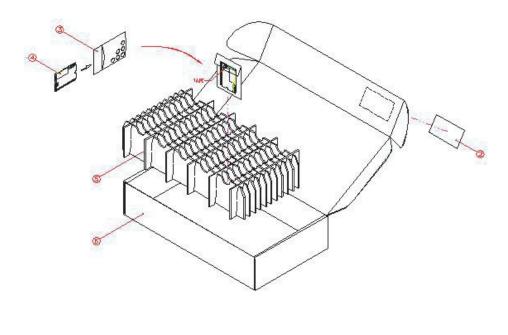
Mechanical Outline

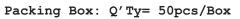


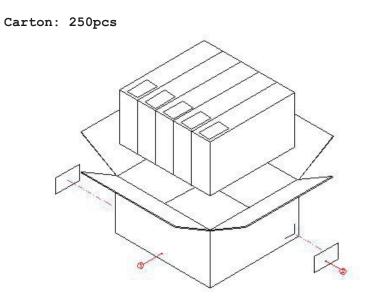
Pin Definition	1:			
Pin Number	Pin Name	Pin I/O Type Description		
1	TIP	NC No use		
2	RING	NC	No use	
3	8PMJ-3	NC	No use	
4	8PMJ-1	NC	No use	
5	8PMJ-6	NC	No use	
6	8PMJ-2	NC	No use	
7	8PMJ-7	NC	No use	
8	8PMJ-4	NC	No use	
9	8PMJ-8	NC	No use	
10	8PMJ-5	NC	No use	
11	LED1_GRNP	Output, 12mA	LED anode	
12	LED2_YELP	Output, 12mA	No use	
13	LED1_GRNN	Input, LED	anode	
14	LED2_YELN	Input,	No use	
15	CHSGND	Ground	Digital Ground	
16	RESERVED	NC	Reserved	
17	INTB#	NC	No use	
18	5V	NC	5V, no use	
19	3.3V	Power	3.3V±10%	
20	INTA#	CMOS, Output	PCI bus Interrupt A	
21	RESERVED	NC	Reserved	
22	RESERVED	NC	Reserved	
23	GROUND	Ground	Digital ground	
24	3.3VAUX	Power	3.3V±10%	
25	CLK	Input, Weak pull down	Providing timing for all transactions on the PCI bus	
26	RST#	Input, Weak pull up	PCI reset	
27	GROUND	Ground	Digital ground	
28	3.3V	Power	3.3V±10%	
29	REQ#	Output	PCI bus request	
30	GNT#	Input, Weak pull high	PCI bus grant	
31	3.3V	Power	3.3V±10%	
32	GROUND	Ground	Digital ground	
33	AD[31]	BiDir,, Weak pull down	PCI address/data bus bit 31	
34	PME#	Output	Power Management Event Output	
35	AD[29]	BiDir,, Weak pull down	PCI address/data bus bit 29	
36	RESERVED	BT_ACTIVE	Reserved	

Pin Definition:				
Pin Number	Pin Name	Pin I/O Type	Description	
37	GROUND	Ground	Digital ground	
38	AD[30]	BiDir,, Weak pull down PCI address/data bus bit 30		
39	AD[27]	BiDir,, Weak pull down	PCI address/data bus bit 27	
40	3.3V	Power 3.3V±10%		
41	AD[25]	BiDir,, Weak pull down	PCI address/data bus bit 25	
42	AD[28]	BiDir,, Weak pull down	PCI address/data bus bit 28	
43	RESERVED	RX CLEAR (WLAN)	Reserved	
44	AD[26]	BiDir,, Weak pull down	PCI address/data bus bit 26	
45	C/BE[3]#	BiDir,, Weak pull up	PCI bus commands and byte 3 enables	
46	AD[24]	BiDir,, Weak pull down	PCI address/data bus bit 24	
47	AD[23]	BiDir,, Weak pull down	PCI address/data bus bit 23	
48	IDSEL	Input, Weak pull down	Initialization device select	
49	GROUND	Ground	Digital ground	
50	GROUND	Ground	Digital ground	
51	AD[21]	BiDir,, Weak pull down	PCI address/data bus bit 21	
52	AD[22]	BiDir,, Weak pull down	PCI address/data bus bit 22	
53	AD[19]	BiDir,, Weak pull down	PCI address/data bus bit 19	
54	AD[20]	BiDir,, Weak pull down	PCI address/data bus bit 20	
55	GROUND	Ground	Digital ground	
56	PAR	BiDir, Weak pull up	PCI bus parity	
57	AD[17]	BiDir,, Weak pull down	PCI address/data bus bit 17	
58	AD[18]	BiDir,, Weak pull down	PCI address/data bus bit 18	
59	C/BE[2]#	BiDir,, Weak pull up PCI bus commands and byte 2 enables		
60	AD[16]	BiDir,, Weak pull down PCI address/data bus bit 16		
61	IRDY#	BiDir,, Weak pull up	PCI initiator ready	
62	GROUND	Ground	Digital ground	
63	3.3V	Power	3.3V±10%	
64	FRAME#	BiDir,, Weak pull down	PCI frame.	
65	CLKRUN#	Input, Weak pull up	Control signal for PCI clock	
66	TRDY#	BiDir,, Weak pull up PCI target ready		
67	SERR#	BiDir, Weak pull up PCI system error		
68	STOP#	BiDir, Weak pull up PCI cycle stop signal		
69	GROUND	Ground	Digital ground	
70	3.3V	Power	3.3V±10%	
71	PERR#	BiDir, Weak pull up	PCI bus parity	
72	DEVSEL#	BiDir, Weak pull up	PCI device select	
73	C/BE[1]#	BiDir, Weak pull down	PCI bus commands and byte 1 enables	
74	GROUND	Ground	Digital ground	
75	AD[14]	BiDir, Weak pull down	PCI address/data bus bit 14	
76	AD[15]	BiDir, Weak pull down		
77	GROUND	Ground BiDir Wook pull down	Digital ground	
78	AD[13]	BiDir, Weak pull down	PCI address/data bus bit 13	
79	AD[12]	BiDir, Weak pull down	PCI address/data bus bit 12	
80 81	AD[11]	BiDir, Weak pull down	PCI address/data bus bit 11 PCI address/data bus bit 10	
81	AD[10] GROUND	BiDir, Weak pull down Ground		
83	GROUND	Ground	Digital ground Digital ground	
84	AD[09]	BiDir, Weak pull down	PCI address/data bus bit 9	
85	AD[08]	BiDir, Weak pull down	PCI address/data bus bit 8	
86	C/BE[0]#	BiDir, Weak pull up	PCI bus commands and byte 0 enables	
87	AD[07]	BiDir, Weak pull down	PCI address/data bus bit 7	
88	3.3V	Power	3.3V±10%	
89	3.3V	Power	3.3V±10%	
90	AD[06]	BiDir, Weak pull down	PCI address/data bus bit 6	
91	AD[05]	BiDir, Weak pull down	PCI address/data bus bit 5	
31	22[00]	Sibil, Weak pull down	· • · · · · · · · · · · · · · · · · · ·	

Pin Definitior	1:		
Pin Number	Pin Name	Pin I/O Type	Description
92	AD[04]	BiDir, Weak pull down	PCI address/data bus bit 4
93	RESERVED	NC	Reserved
94	AD[02]	BiDir, Weak pull down	PCI address/data bus bit 2
95	AD[03]	BiDir, Weak pull down	PCI address/data bus bit 3
96	AD[00]	BiDir, Weak pull down	PCI address/data bus bit 0
97	5V	NC	No use
98	RESERVED_WIP4	NC	Reserved
99	AD[01]	BiDir, Weak pull down	PCI address/data bus bit
100	RESERVED_WIP4	NC	Reserved
101	GROUND	Ground	Digital ground
102	GROUND	Ground	Digital ground
103	AC_SYNC	NC	No use
104	M66EN	NC	PCI 66MHz Enable, no use
105	AC_SDATA_IN	NC	No use
106	AC_SDATA_OUT	NC	No use
107	AC_BIT_CLK	NC	No use
108	AC_CODEC_ID0#	NC	No use
109	AC_CODEC_ID1#	NC	No use
110	AC_RESET#	NC	No use
111	MOD_AUDIO_MON	NC	No use
112	RESERVED	NC	Reserved
113	AUDIO_GND	Ground	Analog ground
114	GROUND	Ground	Digital ground
115	SYS_AUDIO_OUT	NC	No use
116	SYS_AUDIO_IN	NC	No use
117	SYS_AUDIO_OUT GND	NC	No use
118	SYS_AUDIO_IN GND	NC	No use
119	AUDIO_GND	NC	No use
120	AUDIO_GND	Ground	Analog ground
121	RESERVED	NC	Reserved
122	MPCIACT#	NC	Mini PCI function active, no support
123	VCC5VA	NC	No use
124	3.3VAUX	Power	3.3V±10%







Specifications:				
Standard Conformance	IEEE 802 11b 8	02 11a		
Frequency Range	IEEE 802.11b, 802.11g 2.400 ~ 2.4835GHz for US, Canada, Japan, ETSI, and China			
Channel Spacing	5MHz			
Interface				
Operation Voltage	half-size mini-PC 3.3VDC ± 10%			
Modulation Technique	• 802.11b:			
modulation rechnique		CCK, DQPSK, and DBPSK		
	• 802.11g:			
		h BPSK, QPSK, 16-QAM, and 64 n CCK, DQPSK, and DBPSK	I-QAM	
Data Rate		nal mode): 11, 5.5, 2, 1Mbps, a	auto-fallback	
	• 802.11g (norn	nal mode): 54, 48, 36, 24, 18,	12, 9, 6Mbps, auto-fallback	
		er mode): up to 108Mbps		
Operating Range	• 802.11b	over 300 meters @ 11Mbps		
(subject to the environment and antenna)		5-100 meters @ 11Mbps		
and antenna)	• 802.11g			
		over 300 meters @ 6Mbps 5-100 meters @ 6Mbps		
Operating Channels	• 10007:35	Too merers @ omphs		
operaning enamere	.,,,	da: 11 (1~11)		
		ope Countries: 13 (1~13)		
	 France: 4 Japan: 14 	(10~13) for 802.11b (1~13 or 14th), 1	3 for 802.11g (1~13)	
	• China: 13			
Power Consumption		802.11b	802.11g	
		Typical/Max. (mA)	Typical/Max. (mA)	
	Continue Tx	568/607	563/613	
	Continue Rx	205/274	205/274	
	Standby mode	typical 203/28, max. 210/32	typical 206/26, max. 218/27	
	Power saving	typical 207/19, max. 212/21	typical 207/19, max. 212/21	
	Radio off	19/21	19/21	
			19/21	
Transmit Power Settings	 +19 dBm@ 1, +18 dBm@ 6, 			
	 +17dBm@ 36l 			
	• +15dBm@48№			
Considuity	• +13dBm@54№	10ps		
Sensitivity	• 802.11b □ 2412: -95	~-91dBm@1Mbps , -87~-83dB	m@11Mbps	
	• 2442: -94	~-90dBm@1Mbps, -86~-82dB	Bm@11Mbps	
		~-91dBm@1Mbps , -87~-83dB	Bm@11Mbps	
	• 802.11g	~-85dBm@1Mbps , -72~-68dB	3m@11Mbps	
	• 2442: -88	~-84dBm@1Mbps , -71~-67dB	Bm@11Mbps	
		~-85dBm@1Mbps , -72~-68dB		
Antenna MAC Protocol	two U.FL antenna connectors for diversity function			
Security	CSMA/CA with ACK architecture 32-bit MAC 64-bit, 128-bit and 152-bit WEP encryption 			
		itication, WPA, and WPA2		
	• 128-bit AES &	TKIP encryption		
One section Duratema Duran arted	CCX3.0	Windows Mo. Windows 2000	Mindows VD Mindows Mista MAL	
Operation Systems Supported	Linux	windows Me, windows 2000, v	Vindows XP, Windows Vista, MAI	DWIFI
WHQL	Windows 2000, 2	XP		
Wi-Fi Compliance	WECA compliance			
Radio Option	hardware radio (
Dimension		9.2mm(W) x 2.1mm(H)		
Operation Temperature Range	-0°C ~ +60°C	9.2mm(W) x 2.1mm(H)		

Specifications:	
Storage Humidity	max. 90%, non-condensing
Environment-Friendly Compliance	RoHS

Ordering Informations:

DRMA-81 802.11 b/g 54Mbps wifi mini-PCI module, half-size, MB55/AR2417



Unex Technology Corp. - Durable Bridge to Wireless

sales-a@unex.com.tw http://www.unex.com.tw